FPGA Design Flow using Xilinx Vivado 11th and 12th October 2019

Registration forms

CHIEF PATRON:

Dr. M. K. Surappa Vice-Chancellor, AnnaUniversity, Chennai.

PATRON:

Dr. L. Karunamoorthy Registrar, Anna University, Chennai.

CHAIR:

Dr.T.V.Geetha Dean of CEG.

CO-CHAIR:

Dr.S.Muttan Coordinator, UPE

CO-ORDINATORS:

Dr.S.Nirmala Devi, Prof Dr.T.Jayasree Asst. Prof

VENUE:

Mini Auditorium, Department of ECE. College of Engineering Guindy, Anna University, Chennai-600 025.

REGISTRATION FEE

There is no registration fee for the workshop. The participants will be selected on "First come- First served" basis. The completed registration form can be submitted to the coordinator not later than 07.10.2019. The scanned copy of the form can be mailed to the specified e-mail ID. The selection intimation will be sent by e-mail on 09.10.2019.

IMPORTANT DATES:

Submission of Application: 07.10.2019 Intimation of Selection: 08.10.2019 Confirmation by participants: 09.10.2019

ADDRESS FOR COMMUNICATION:

Workshop on "FPGA Design Flow using Xilinx Vivado"
Department of ECE,
College of Engineering Guindy,
Anna University,
Chennai- 600025.

E-mail: upeworkshop@gmail.com

NO ACCOMMODITION WILL BE PROVIDED

REGISTRATION FORM

Name: RADINI LANTHLY

Designation: APIECE

Specialization: Applied Electronic

Areas of research: Medical Image

Department: ECE

College/organization: Adni college of Eng,

Address: watajohad kanchi Puram

Phone No.: 9789676337

E-mail: rajinitan kn. ece @ adhi. edu.in

DECLARTION

The above information is true to the best of my knowledge. I agree to abide by the rules and regulations governing the Workshop of the Anna University. If selected, I shall attend the programme for the entire duration. I also undertake the responsibility to inform the coordinator in advance in case I am unable to attend the workshop.

Date: 10-07.19 Signature of applicant

Place: kankhipuran.

Signature of the HOD Principal with seal

Walajabad, Kanchipuram Dist - 631605

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